•	Application No.	Applicant(s)
Notice of Allowability	09/810,366	COWLES, TIMOTHY B.
	Examiner	Art Unit
	Christine T. Tu	2133
The MAILING DATE of this communication of All claims being allowable, PROSECUTION ON THE MERIT herewith (or previously mailed), a Notice of Allowance (PTOL NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATEN of the Office or upon petition by the applicant. See 37 CFR 1	S IS (OR REMAINS) CLOSED in 85) or other appropriate comm IT RIGHTS. This application is:	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to <u>amendment filed</u>	<u>10/22/2004</u> .	•
2. X The allowed claim(s) is/are 47-49 and 55-57.		
3. The drawings filed on 15 March 2001 are accepted by	the Examiner.	
 4. ☐ Acknowledgment is made of a claim for foreign priority a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents 2. ☐ Certified copies of the priority documents 3. ☐ Copies of the certified copies of the priority International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	have been received. have been received in Application	on No
Applicant has THREE MONTHS FROM THE "MAILING DA noted below. Failure to timely comply will result in ABANDOTHIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ONMENT of this application.	
 A SUBSTITUTE OATH OR DECLARATION must be s INFORMAL PATENT APPLICATION (PTO-152) which 	ubmitted. Note the attached EX gives reason(s) why the oath o	AMINER'S AMENDMENT or NOTICE OF r declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets")	must be submitted.	
(a) ☐ including changes required by the Notice of Drafts	-	w (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date _		
(b) ☐ including changes required by the attached Exam Paper No./Mail Date	iner's Amendment / Comment o	r in the Office action of
Identifying indicia such as the application number (see 37 C each sheet. Replacement sheet(s) should be labeled as such	FR 1.84(c)) should be written on t	he drawings in the front (not the back) of FR 1.121(d).
 DEPOSIT OF and/or INFORMATION about the d attached-Examiner's comment regarding REQUIREME 	eposit of BIOLOGICAL MAT ENT FOR THE DEPOSIT OF BIO	ERIAL must be submitted. Note the DLOGICAL MATERIAL.
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. ☐ Notice of In	formal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-9	48) 6. ☐ Interview S	ummary (PTO-413).
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/	_	/Mail Date
Examiner's Comment Regarding Requirement for Depo Replacing Metarics	_	Statement of Reasons for Allowance
of Biological Material	9.	Christine T. TU Primary Examiner

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Charles Brantley on January 21, 2005.

The application has been amended as follows:

Cancel claim 54;

Claim 55: At line 1, change the number "54" to – 47 --.

2. The following is an examiner's statement of reasons for allowance:

The present invention pertains a circuit for a semiconductor chip including a memory array and an address latch wherein the chip is configured to communicate with a terminal of a tester. The prior arts teach a circuit comprising a comparator for comparing a first data value from the memory array and a second data value from said terminal, and a register for storing the address in response to receiving a signal from the comparator based upon a lack of identity between the first and the second data values. The prior arts, however, do not teach the combination of a comparator, a register and an output circuit within a circuit for the semiconductor chip, such that the output circuit

comprises an inverter coupled to the memory array, a first transistor electrically interposed between the inverter and a positive voltage source and configured to turn off during a test mode for the semiconductor chip, and a second transistor electrically interposed between the inverter and ground and configured to turn off during the test mode.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christine T. Tu whose telephone number is (571)272-3831. The examiner can normally be reached on Mon-Thur. 8:30am-6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert DeCady can be reached on (571)272-3819. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Christine T. Tu
Primary Examiner
Art Unit 2133

January 21, 2005